HOFFMAN WARNICK D ALESSANRO LLC #0586 P.003

AUG. 05'2004 13:20 518 449 0047

Amendments to the Claims

This listing of claims replaces all prior versions, and listings, of claims of the

application.

1. (Currently amended) A method of manufacturing a semiconductor device (10) which is

suitable for surface mounting and which includes a semiconductor body (1) comprising

an active element provided with connection regions (2) situated at the surface of the

semiconductor body (1), an electrically insulating layer (3) being provided, on one of its

sides, with a conductor pattern (4) which is suitable for surface mounting, and, on the

other side, with the semiconductor body (1), the connection regions (2) of the active

element being connected to the conductor pattern (4) by means of electrically conductive

vias (5) in the insulating layer (3), the method including the steps of:

forming the conductor pattern (4) and the insulating layer (3) on using a flexible

foil (6), wherein the flexible foil (6) originally comprises a conductive layer and an

electrically insulating layer, and wherein the conductor pattern is formed from the

conductive layer of the flexible foil and the insulating layer is formed from the

electrically insulating layer of the flexible foil; and

detachably securing the flexible foil (6) to a substrate (7) on the side of the

conductor pattern (4).

2. (Currently amended) A method as claimed in claim 1, characterized in that wherein the

electrically conductive vias (5) are formed by providing the insulating layer (3) with

Serial No.: 10/067,425

2

PAGE 3/10 * RCVD AT 8/5/2004 2:19:39 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/4 * DNIS:8729306 * CSID:518 449 0047 * DURATION (mm-ss):02-44

HOFFMAN WARNICK D ALESSANRO LLC #0586 P.004

AUG. 05'2004 13:20 518 449 0047

apertures, as a result of which parts of the conductor pattern (4) in the conductive layer

are exposed, and introducing solder (8) into said apertures, as a result of which the

connection regions (2) of the semiconductor body (1) are electrically connected to the

conductor pattern (4).

3. (Currently Amended) A method as claimed in claim 1, characterized in that wherein

prior to the formation of the electrically conductive vias (5) and prior to the provision of

the semiconductor body (1) on the flexible foil (6) that is detachably secured to the

substrate (7), the flexible foil (6) is detachably secured, on the side of the insulating layer

(3), to another substrate (9), after which the conductor pattern (4) is formed in the

conductive layer, whereafter the flexible foil (6) is detachably secured, on the side of the

conductive conductor pattern (4), to the substrate (7), after which the other substrate (9) is

removed.

4. (Cancelled).

5. (Currently amended) A method as claimed in claim 1, characterized in that wherein

prior to the provision of the semiconductor body (1) on the flexible foil (6), a part thereof

that is situated between the conductive vias (5) in the insulating layer (3) is provided with

an electrically insulating fixing agent (12) on which the semiconductor body (1) is placed

and the height of which determines the distance between the semiconductor body (1) and

the flexible foil (6).

Serial No.: 10/067,425

3

PAGE 4/10 * RCVD AT 8/5/2004 2:19:39 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/4 * DNIS:8729306 * CSID:518 449 0047 * DURATION (mm-ss):02-44

HOFFMAN WARNICK D ALESSANRO LLC #0586 P.005

AUG.05'2004 13:20 518 449 0047

6-7. (Cancelled).

8. (Currently amended) A method as claimed in claim 1, characterized in that wherein a

solid photoresist material is used for the electrically insulating layer (3).

9. (Currently amended) A method as claimed in claim 1, characterized in that wherein for

the material of the electrically insulating layer (3) use is made of a polyimide, and for the

material of the conductive conductor pattern (4) use is made of copper.

10. (Currently Amended) A semiconductor device (10) which can suitably be used for

surface mounting and which is obtained using a method as claimed in claim 1.

11. (Previously Presented) A method of manufacturing a semiconductor device which is

suitable for surface mounting and which includes a semiconductor body comprising an

active element provided with connection regions situated at the surface of the

semiconductor body, an electrically insulating layer being provided, on one of its sides,

with a conductor pattern which is suitable for surface mounting, and, on the other side,

with the semiconductor body, the connection regions of the active element being

connected to the conductor pattern by means of electrically conductive vias in the

insulating layer, the method including the steps of:

Serial No.: 10/067,425

AUG.05'2004 13:20 518 449 0047 HOFFMAN WARNICK D ALESSANRO LLC #0586 P.006

forming the conductor pattern and the insulating layer on a flexible foil, wherein

the flexible foil originally comprises a conductive layer and an electrically insulating

layer;

detachably securing the flexible foil to a substrate on the side of the insulating

layer; and

detachably securing the flexible foil to a substrate on the side of the conductor

pattern.

12. (Previously Presented) A method of manufacturing a semiconductor device which is

suitable for surface mounting and which includes a semiconductor body comprising an

active element provided with connection regions situated at the surface of the

semiconductor body, an electrically insulating layer being provided, on one of its sides,

with a conductor pattern which is suitable for surface mounting, and, on the other side,

with the semiconductor body, the connection regions of the active element being

connected to the conductor pattern by means of electrically conductive vias in the

insulating layer, the method including the steps of:

forming the conductor pattern and the insulating layer on a flexible foil, wherein

the flexible foil originally comprises a conductive layer and an electrically insulating

layer;

detachably securing the flexible foil to a substrate on the side of the conductor

pattern;

Serial No.: 10/067,425

5

PAGE 6/10 * RCVD AT 8/5/2004 2:19:39 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/4 * DNIS:8729306 * CSID:518 449 0047 * DURATION (mm-ss):02-44

AUG.05'2004 13:21 518 449 0047

providing an electrically insulating fixing agent between the conductive vias in the insulating layer; and

placing the semiconductor body on the electrically insulating fixing agent, wherein a height of the electrically insulating fixing agent determines a distance between the semiconductor body and the flexible foil.

Serial No.: 10/067,425